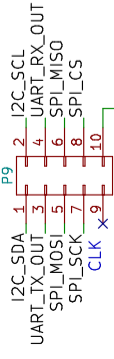
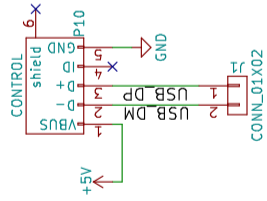


Designed for a cheap FXZLA to be just "plugged in" for sigrok

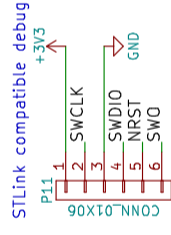


NOTE: this can be freely routed to do whatever's possible! Potentially: Jumpers to select what you're connecting here? (but non-ideal)

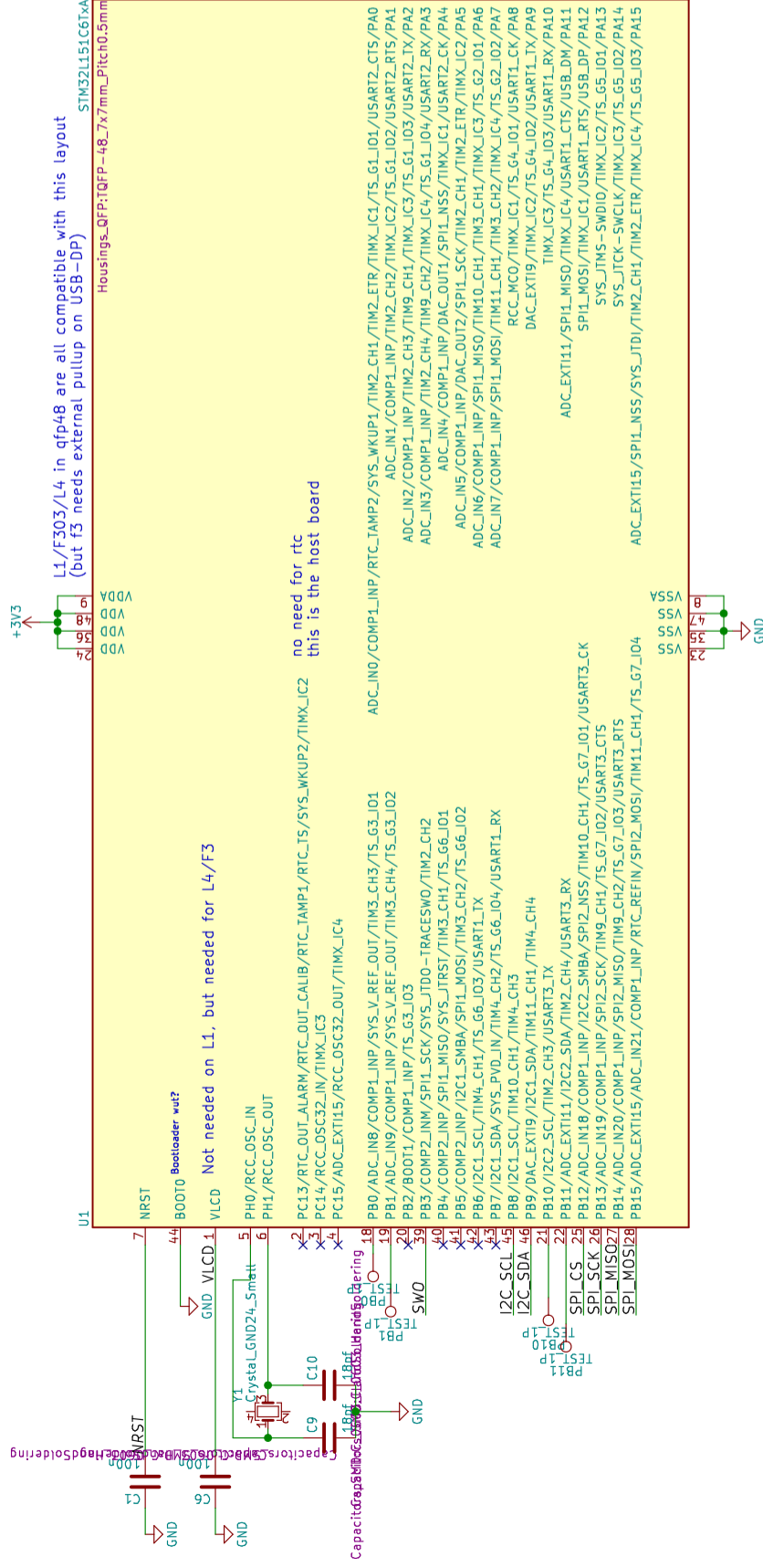
**ONLY GROUND BETWEEN BOARDS by default!**  
Intended for both to have their own USB  
Option for debug only, with power FROM target



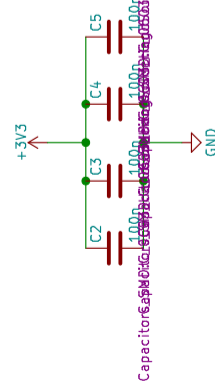
CONN\_01X02  
Probe points, for bus tracing USB



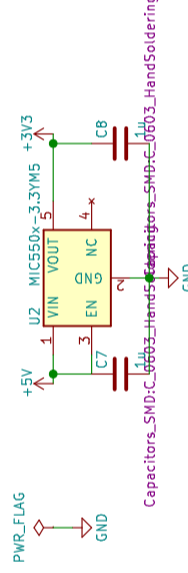
[X] debug via via 6pin  
[ ] debug via 10x5x1.27mm cortex SMD



no need for rtc this is the host board

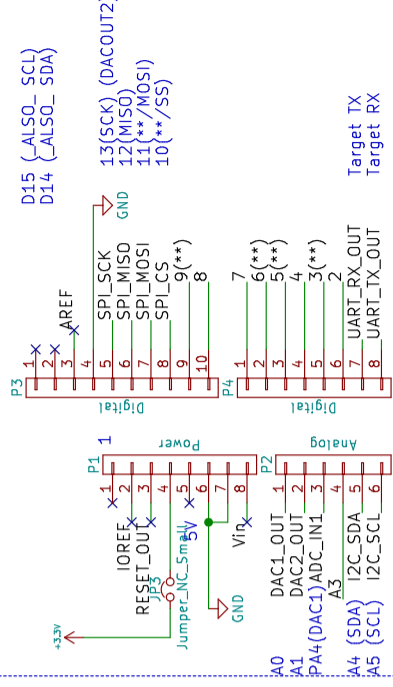


Capacitors should be placed as close as possible to the pins being soldering

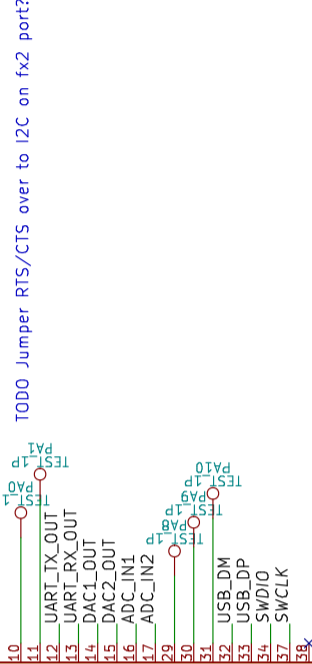


Capacitors\_SMD: C6603+HandSoldering

Arduino UNO-3 pins  
As used on Nucleo64 boards

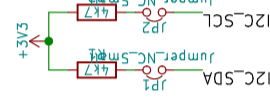


TODO  
[ ] Route all spare pins to something\_?



TODO Jumper RTS/CTS over to I2C on fx2 port?

IN/OUT is from the perspective of HOST  
DUT Nucleo is TARGET



libopenocm3  
Sheet: /  
File: hw1.sch

Title: test host

Size: A3

Date: Autumn 2017

KiCad E.D.A. kicad 4.0.6

Rev: 1/1

Id: 1/1